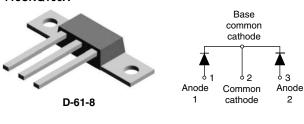
Vishay High Power Products

Schottky Rectifier New Generation 3 D-61 Package, 2 x 55 A

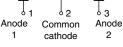
113CNQ100A

ISHA



113CNQ100ASM

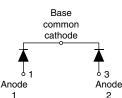






113CNQ100ASL





PRODUCT SUMMARY				
I _{F(AV)}	2 x 55 A			
V _R	100 V			

FEATURES

- 175 °C T_J operation
- Center tap module
- Low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- New fully transfer-mold low profile, small footprint, high current package
- Designed and qualified for industrial level

DESCRIPTION

The 113CNQ100A center tap Schottky rectifier module series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	110	А		
V _{RRM}		100	V		
I _{FSM}	$t_p = 5 \ \mu s \ sine$	7000	А		
V _F	55 Apk, $T_J = 125 \ ^\circ C$ (per leg)	0.67	V		
TJ	Range	- 55 to 175	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	113CNQ100A	UNITS	
Maximum DC reverse voltage	V _R	100	V	
Maximum working peak reverse voltage	V _{RWM}		v	

113CNQ100A

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ABSOLUTE MAXIMUM RATINGS						
PARAMETER		SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	per leg				55	А
See fig. 5	per device	I _{F(AV)}	50% unity cycle at $1c = 150%$ C, rectangular wavelonn		110	
Maximum peak one cycle			5 μs sine or 3 μs rect. pulse	Following any rated load condition and with	7000	•
non-repetitive surge current per leg I _{FS} See fig. 7		IFSM	IFSM 10 ms sine or 6 ms rect. pulse	rated V_{RRM} applied	720	A
Non-repetitive avalanche energy per leg E_{AS} $T_J = 25 \text{ °C}, I_{AS} = 1 \text{ A}, L = 30 \text{ mH}$		1	15	mJ		
Repetitive avalanche curren	t per leg	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		1	A

ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	L TEST CONDITIONS VALUES		UNITS	
Maximum forward voltage drop per leg See fig. 1	V _{FM} ⁽¹⁾	55 A	T _J = 25 °C	0.81	V
		110 A		1.00	
		55 A	- T _J = 125 °C	0.66	
		110 A		0.79	
Maximum reverse leakage	(1)	T _J = 25 °C	V _R = Rated V _R	1.0	mA
urrent per leg I _{RM} ⁽¹⁾ ee fig. 2	IRM (')	T _J = 125 °C		32	
Maximum junction capacitance per leg	CT	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		1960	pF
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body		5.5	nH
Maximum voltage rate of change	dV/dt	Rated V _R 10 000		10 000	V/µs

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range	T _J , T _{Stg}		- 55 to 175	°C	
Maximum thermal resistance, junction to case per leg		DC operation See fig. 4	0.5	°C/W	
Maximum thermal resistance, junction to case per package	R _{thJC}	DC operation	0.25		
Typical thermal resistance, case to heatsink (D-61-8 only)	R _{thCS}	Mounting surface, smooth and greased Device flatness < 5 mils	0.30		
Approvimente weight			7.8	g	
Approximate weight			0.28	0Z.	
Mounting torque minimum		Decommended herdwore 2M steinlass corour	12 (10)	kgf ⋅ cm	
(D-61-8 only) maximum		Recommended hardware 3M stainless screw	24 (20)	(lbf · in)	
		Case style D-61-8	113CN	Q100A	
Marking device		Case style D-61-8-SM	113CNQ	100ASM	
		Case style D-61-8-SL	113CNC	100ASL	

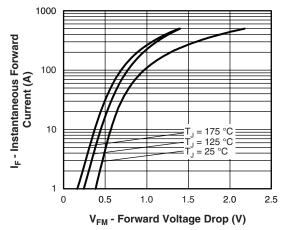
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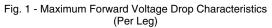


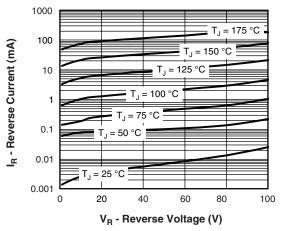
113CNQ100A

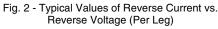
Schottky Rectifier V New Generation 3 D-61 Package, 2 x 55 A

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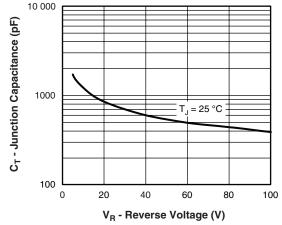


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

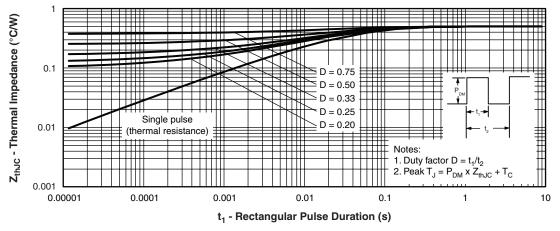
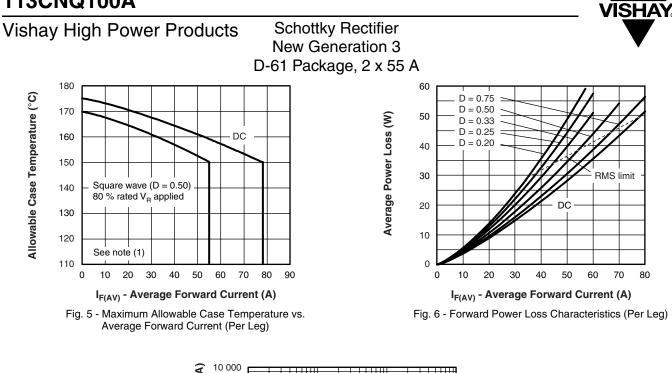


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics (Per Leg)

113CNQ100A



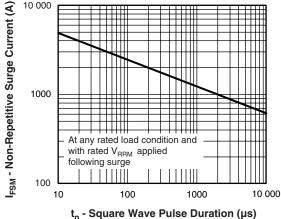


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

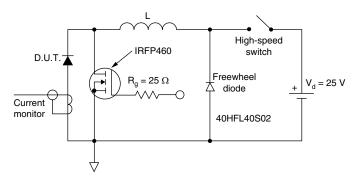


Fig. 8 - Unclamped Inductive Test Circuit

Note

(1) Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

 $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \ x \ \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \ x \ \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{80} \ \% \ \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$



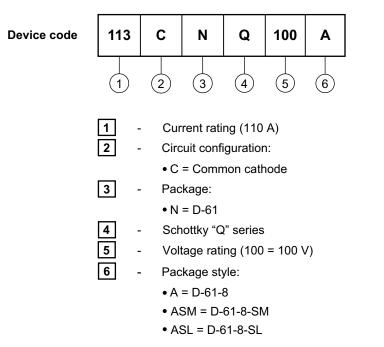




Schottky Rectifier New Generation 3

D-61 Package, 2 x 55 A

ORDERING INFORMATION TABLE



Standard pack quantity: A = 10 pieces; ASM/ASL = 20 pieces

LINKS TO RELATED DOCUMENTS			
Dimensions http://www.vishay.com/doc?95354			
Part marking information	http://www.vishay.com/doc?95356		



Vishay

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